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Cofc

Patent
Attorney's Docket No. <u>0119-012</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of) Jiren YUAN) Patent No.: 7,053,673 B1) Issued: May 30, 2006) For: **VERSATILE CHARGE SAMPLING)** **CIRCUITS**)	**ATTN: Certificate of Corrections** **Branch**

REQUEST FOR CERTIFICATE OF CORRECTION UNDER 37 CFR 1.322
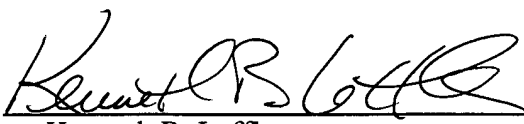
Commissioner for Patents
Alexandria, VA 22313-1450

Sir:

Applicant respectfully requests a Certificate of Correction for the errors indicated on the attached form PTO/SB/44. It is believed that these errors occurred on behalf of the Patent Office, therefore no fee is attached.

Respectfully submitted,

POTOMAC PATENT GROUP PLLC

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Kenneth B. Leffler
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Date: _November 9, 2006_

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION
PAGE 1 OF 2

PATENT NO.   :   7,053,673 B1

DATED   :   May 30, 2006

INVENTOR(S)   :   Jiren YUAN

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 5, Line 3: Change "$Q_1=(2 \sin(\omega_1\Delta\omega_i)I_i \sin$" to -- $Q_1=(2\sin(\omega_1\Delta t/\omega_i)I_i \sin$--

Column 5, Line 15: Change "GHZ" to --GHz--

Column 6, Line 60: Change "$n\leq10$" to --$n=10$--

Claim 5, Line 6: Change "of the second" to --of the first--

Claim 5, Line 10: Change "outpu" to --output--

Claim 10, Line 13: Change "when $(2p-1)f_{c\text{-}fin1}=f_{in2}-(2p-1)f_c$" to --when $(2p-1)f_c\text{-}f_{in1}=f_{in2}-(2p-1)f_c$ --

Claim 13, Line 13: Change "outpu;" to --output;--

Claim 13, Line 19: Change "connecte" to --connected--

Claim 13, Line 27: Change "the signal outpu" to --the signal output--

Claim 14, Line 6: Change "T-clock" to --I-clock--

Claim 19, Line 1: Change "sampling circuit," to --sampling circuit--

Claim 19, Line 2: Change "sampling circuits" to --sampling circuits,--

MAILING ADDRESS OF SENDER:

PATENT NO. 7,053,673 B1

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION
PAGE 2 OF 2

PATENT NO.　　:　7,053,673 B1

DATED　　:　May 30, 2006

INVENTOR(S)　:　Jiren YUAN

　　　　It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 19, Line 3: Change "each having a differential charge sampling circuit," to --each differential charge sampling circuit--

Claim 19, Line 18 : Change "analog inputs," to --analog inputs;--

Claim 22, Line 16: Change "bandpass" to --band-pass--

Claim 23, Line 15: Change "output pair is" to --output is—

Claim 25, Line 1: Change "A charge sampling," to --A charge sampling circuit,--

Claim 26, Line 2: Change "a first active integrator;" to --a first integrator;--

Claim 26, Line 3: Change "a second active integrator," to --a second integrator;--

Claim 29, Lines 8 & 9 : Change "is from time $t_5==(t_1+t_2)/2$ and" to --is from time t_1 to time t_2, said sample represents the instant value of said analog signal at time $t_s=(t_1+t_2) / 2$ and--

Claim 29, Line 13: Change "$t==(T_2-t_1)/2$." to --$t=(t_2-t_1) /2$.--

MAILING ADDRESS OF SENDER:

PATENT NO. __7,053,673 B1__

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